

Remarks/Arguments

The rejection of claims 1 – 4, 6, 8 – 10, 12, and 13 under 35 U.S.C. § 102(a) as being anticipated by the Intel Server Chassis SBCE and its various components, and the rejection of claims 5, 7, and 11 under 35 U.S.C. § 103(a) as being unpatentable over the Intel Server Chassis SBCE as applied to claims 1 and 8, and further in view of the I²C Specification (January 2000)., are both respectfully traversed.

Submitted herewith is a Claim For Priority And Submission Of Documents, pursuant to which a certified copy of Taiwanese Application serial No. 092108198 (which published as Publication No. TW 224273 B1) is also enclosed, having a filing date of April 10, 2003. Since the priority date of the present application predates the primary reference, the rejections under both 35 U.S.C. § 102(a) and 35 U.S.C. § 103(a) are believed to be obviated.

The Examiner is thanked for bringing applicant's attention to the benefits of claiming the foregoing priority.

No issues remaining

Respectfully submitted,

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